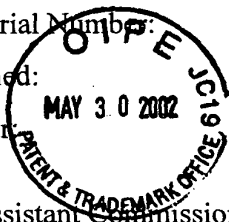


085/02455

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Amnon GANOT, et al. Group Art Unit: 2825  
Serial Number: 10/045,651 Examiner: Unknown  
Filed: November 7, 2001  
For: **Multi-Layer Printed Circuit Board Fabrication System and Method**

Assistant Commissioner for Patents  
Office of Initial Patent Examination  
Customer Service Center  
Washington, DC 20231



**REQUEST FOR CORRECTED FILING RECEIPT**

Attached is an annotated copy of the official filing receipt received from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested.

The first two inventors were erroneously omitted from the filing receipt. The *Applicant(s)* field should read as follows:

**Applicant(s)**

**Amnon Ganot, Bat-Yam, ISRAEL;**

**Hanan Gino, Rishon-Lezion, ISRAEL;**

Golan Hanina, Rishon-Lezion, ISRAEL;

Zeev Kantor, Gan-Yavne, ISRAEL;

Boris Kling, Rehovot, ISRAEL;

Shabtay Spinzi, Tel-Mond, ISRAEL;

Barry Ben-Ezra, Ramat-Hasharon, ISRAEL;

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JUN 12 2002

OFFICE OF PETITIONS

**REMARKS**

Applicants respectfully request a corrected filing receipt in compliance with the declaration as filed.

The corrections are not due to any error by applicants, and no fee is due.

Respectfully submitted,  
Amnon GANOT et al.

May 26, 2002

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## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
www.uspto.gov

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/045,651 ✓	11/07/2001 ✓	2825	0.00 ✓	085/02455 ✓	17 ✓	55 ✓	6 ✓

CONFIRMATION NO. 1909

## FILING RECEIPT



\*OC000000007823227\*

W.H. Dippert  
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1133 Avenue of the Americas  
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Date Mailed: 04/08/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

Applicant(s) Amnon Ganot, Bat-Yam, ISRAEL;  
Hanan Gino Rishon-Lezion, ISRAEL;  
Golan Hanina, Rishon-Lezion, ISRAEL; ✓  
Zeev Kantor, Gan-Yavne, ISRAEL; ✓  
Boris Kling, Rehovot, ISRAEL; ✓  
Shabtay Spinzi, Tel-Mond, ISRAEL; ✓  
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## Domestic Priority data as claimed by applicant

THIS APPLICATION IS A CIP OF 09/792,498 02/23/2001 ✓  
WHICH IS A CIP OF 09/708,160 11/08/2000 ✓

## Foreign Applications

ISRAEL 142354 04/01/2001 ✓

If Required, Foreign Filing License Granted 04/04/2002

Projected Publication Date: To Be Determined - pending completion of Missing Parts.

Non-Publication Request: No

Early Publication Request: No

## Title

Multi-layer printed circuit board fabrication system and method ✓

